

XR88C92/192 or XR68C92/192 Operational Manual
For Rev 2.0 Evaluation Board

Rev .3

Introduction:

This manual is for our evaluation board XR88C92/192 and XR68C92/192. See table 1 for supported packages.

Table 1

EXAR PART	PACKAGE	LOCATION
XR88C92CP	DIP	U3
XR88C92IP	DIP	U3
XR88C192CP	DIP	U3
XR88C192IP	DIP	U3
XR68C92CP	DIP	U3
XR68C92IP	DIP	U3
XR68C192CP	DIP	U3
XR68C192IP	DIP	U3
XR88C92CJ	PLCC	U9
XR88C92IJ	PLCC	U9
XR88C192CJ	PLCC	U9
XR88C192IJ	PLCC	U9
XR68C92CJ	PLCC	U9
XR68C92IJ	PLCC	U9
XR68C192CJ	PLCC	U9
XR68C192IJ	PLCC	U9
XR88C92CV	TQFP	U2
XR88C92IV	TQFP	U2
XR88C192CV	TQFP	U2
XR88C192IV	TQFP	U2
XR68C92CV	TQFP	U2
XR68C92IV	TQFP	U2
XR68C192CV	TQFP	U2
XR68C192IV	TQFP	U2

For a list of features, refer to the Data Sheet at www.exar.com.

There are several jumpers that need to be identified, please refer to Table 2.

Table 2

Jumpers	Functions	Comments
J19 1&2	400	Base address
J19 3&4	4E0	Base address
J19 5&6	500	Base address
J19 7&8	5E0	Base address
J2 1&2	IRQ7	Interrupt
J2 3&4	IRQ5	Default, need to change in driver
J2 5&6	IRQ4	Interrupt
J2 7&8	IRQ3	Interrupt
J3 1&2	Reset	Reset/-reset
J4,5,6,7,8,9,10,11, 20,21,22,23	NOT USED	Factory use only
J12 1&2	UART Selected Vreg	Hardware not installed
J12 3&4	UART Select +3.3V	Select voltage UART
J12 5&6	UART Select +5V	Select voltage UART
J13 1&2	VCC_Max	Select +3.3V to RS232 drivers Default, Cut trace before changing (solder side)
J13 3&2	VCC_Max	Select +5V to RS232 drivers Cut trace before changing (solder side)
J14	Local Loop test	TX1 to RX1
J15	EIA Local Loop test	TXD1 to RXD1
J16	Local Loop test	TX2 to RX2
J24	EIA Local Loop test	TXD2 to RXD2
J18	88/-68 Mode	Default 88 mode
J17	JTAG-CPLD	Factory use only
P1	EIA Output	Channel 1
P2	EIA Output	Channel 2