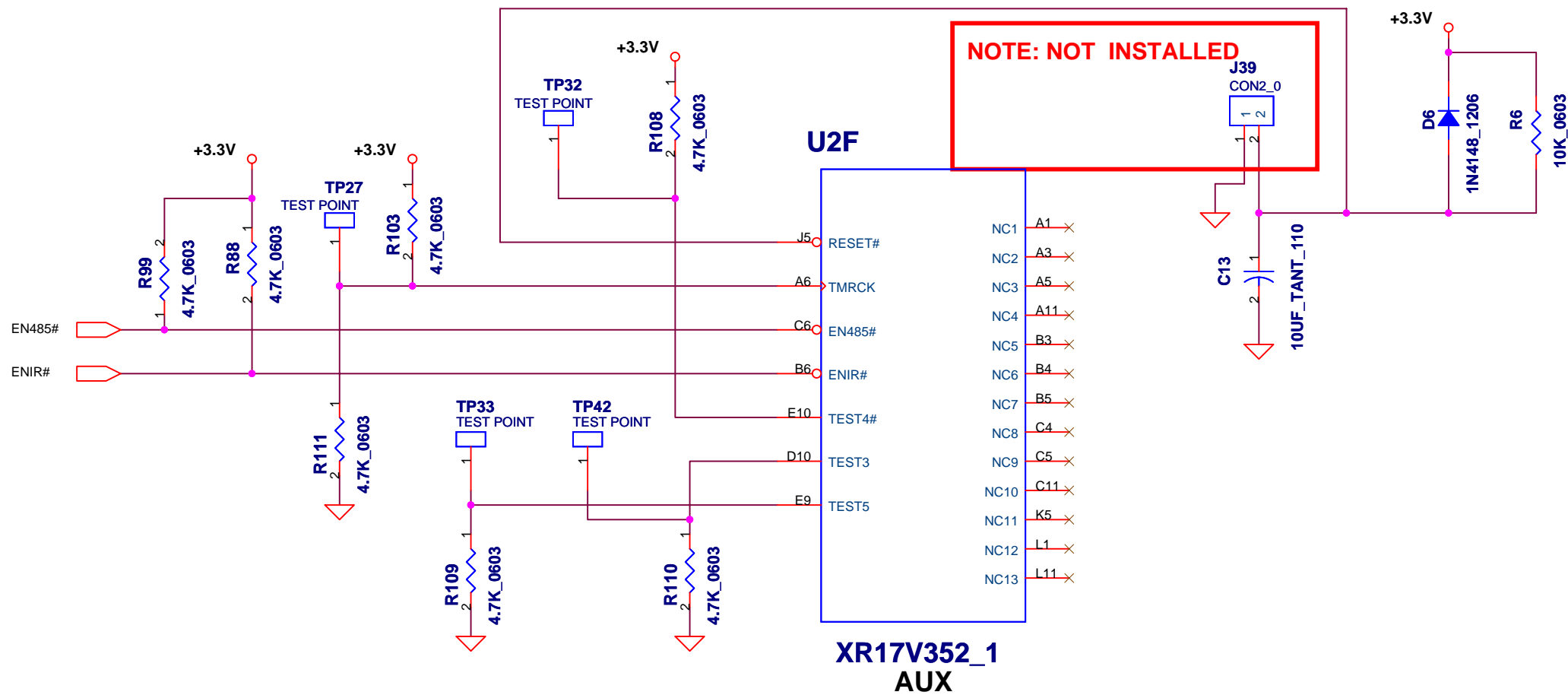
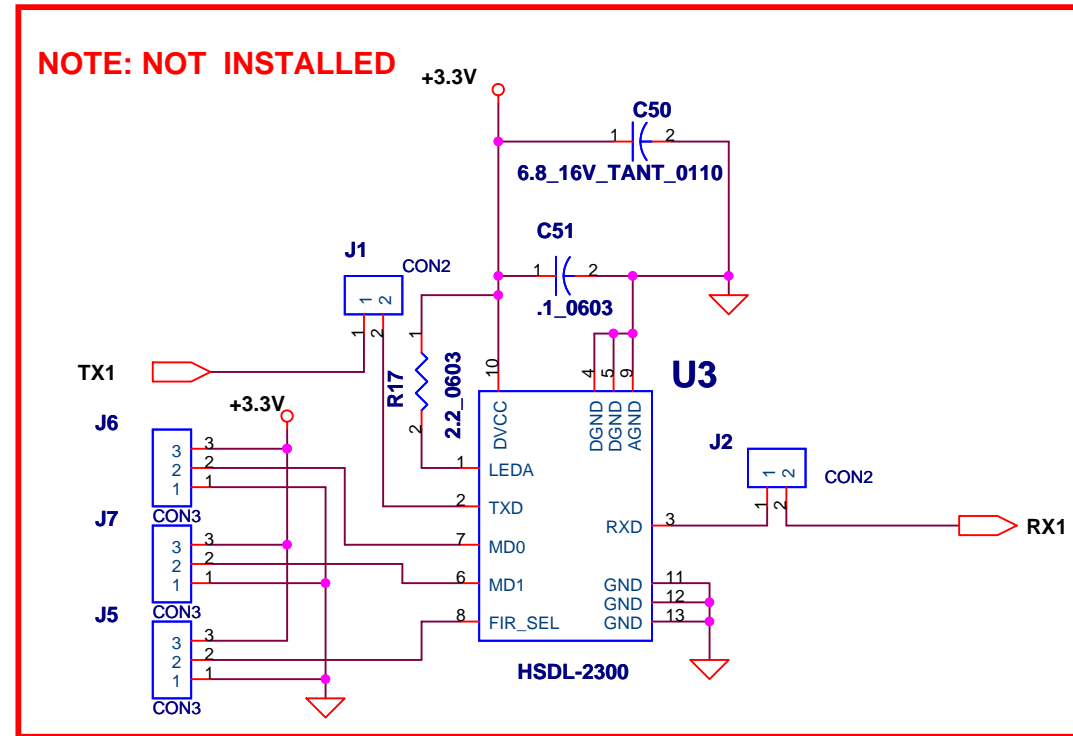
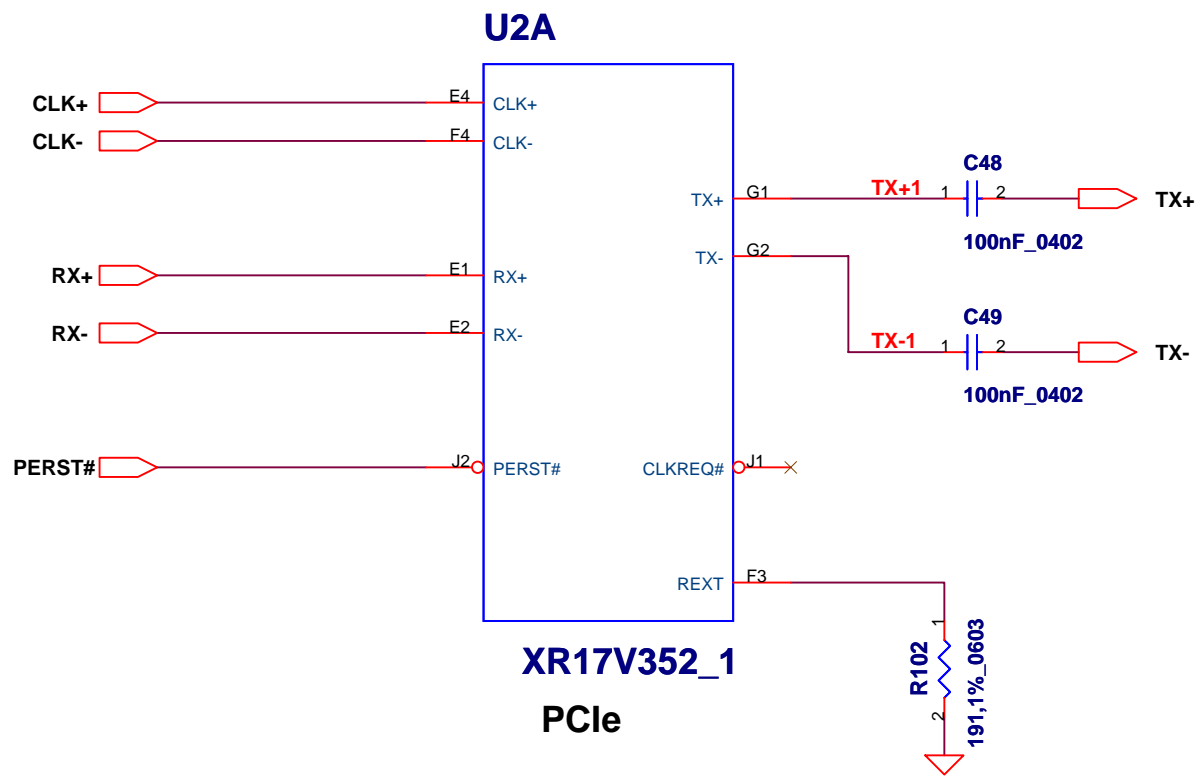
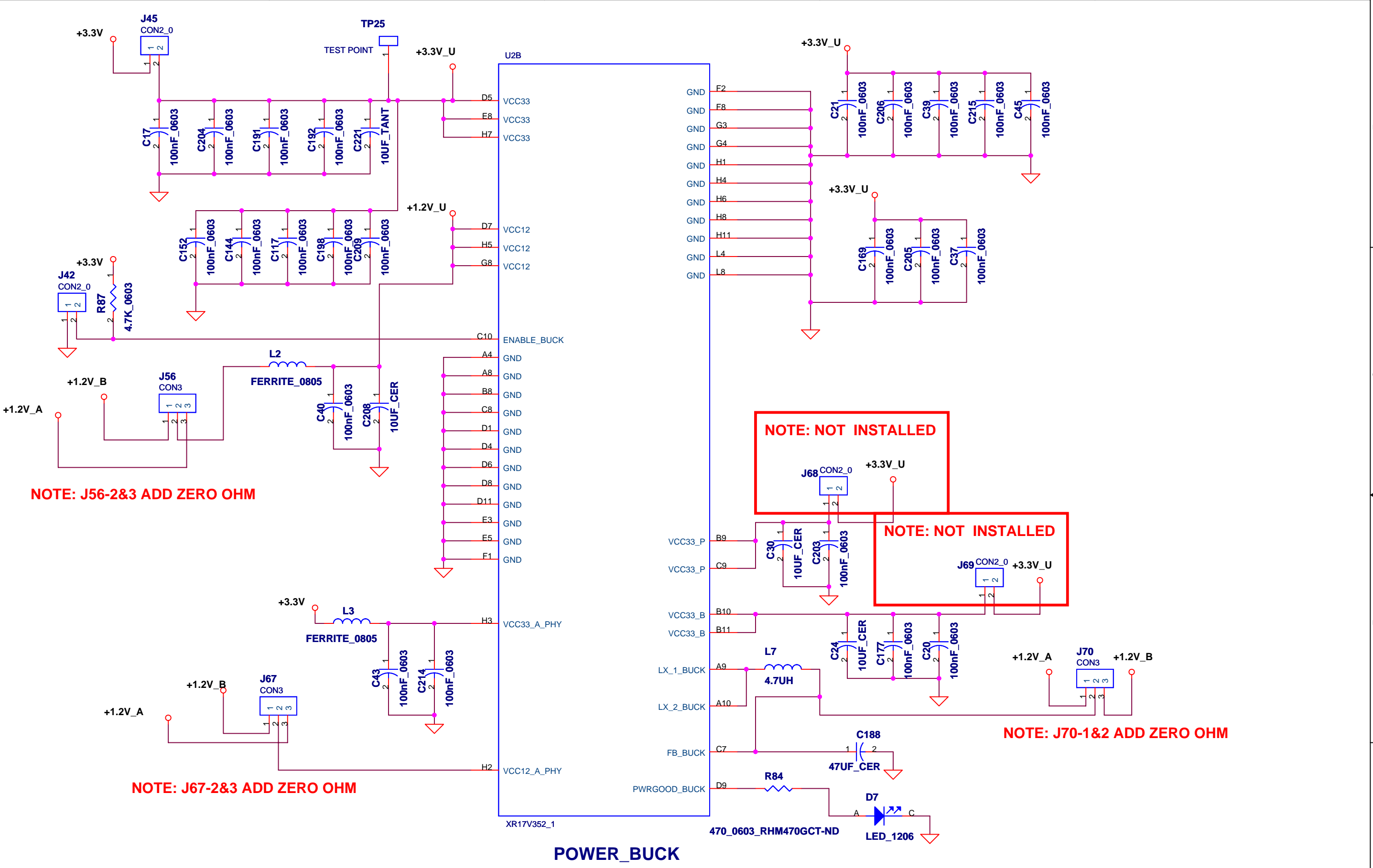


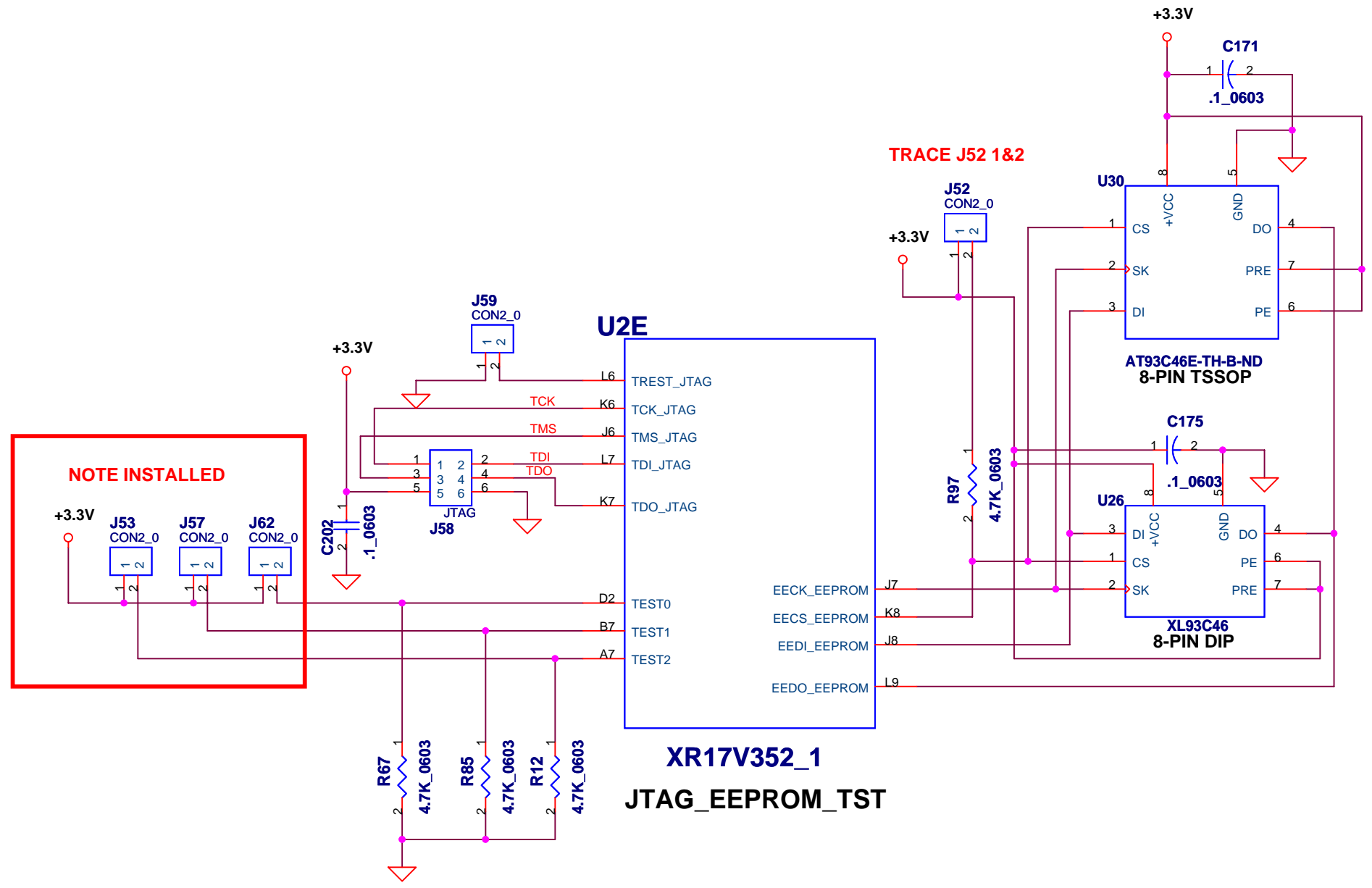
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PCI INTERFACE		
Size	Document Number	Rev
	17V352 EVALUATION BOARD	1.0
Date:	Friday, December 11, 2009	Sheet 1 of 9



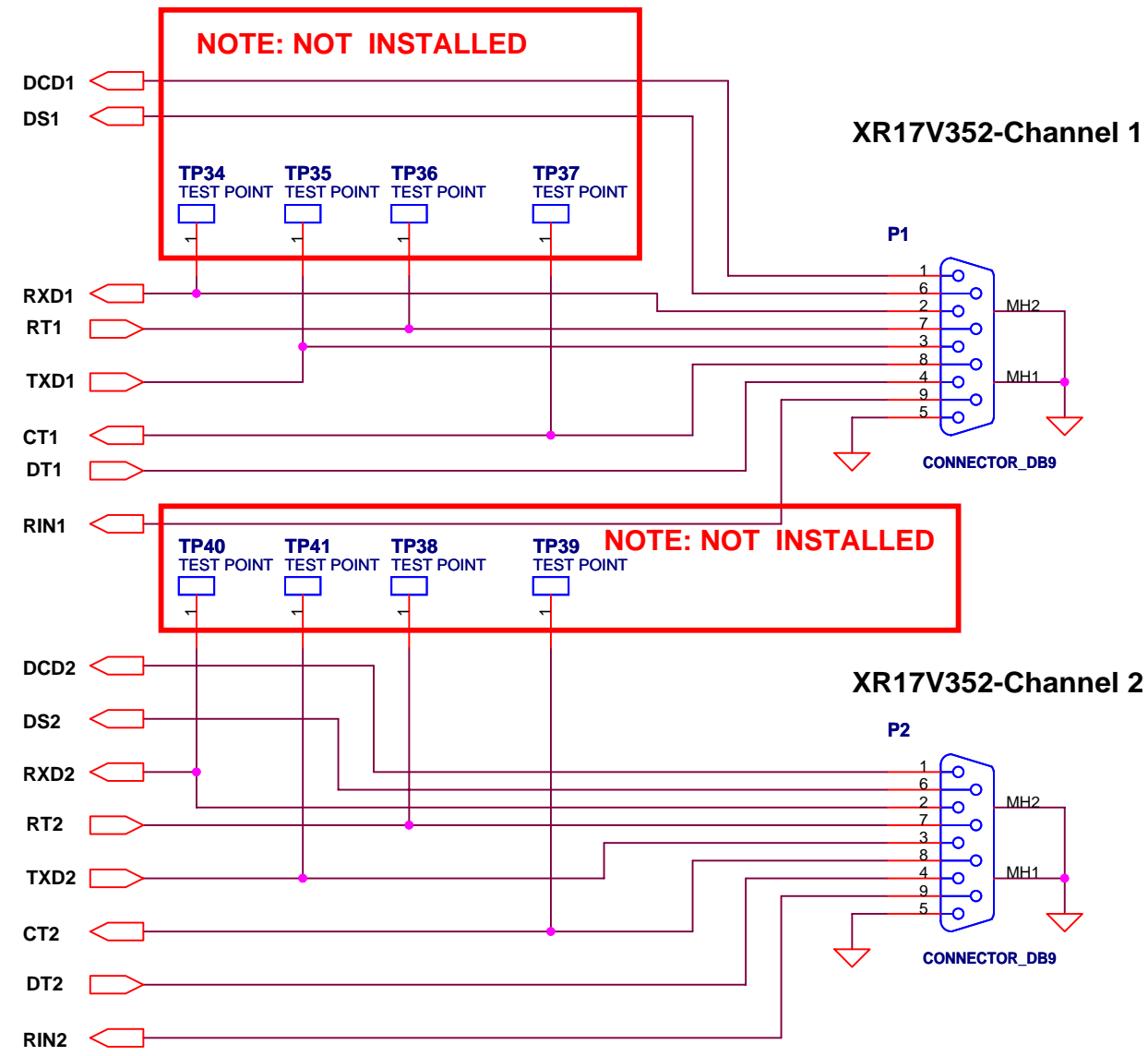
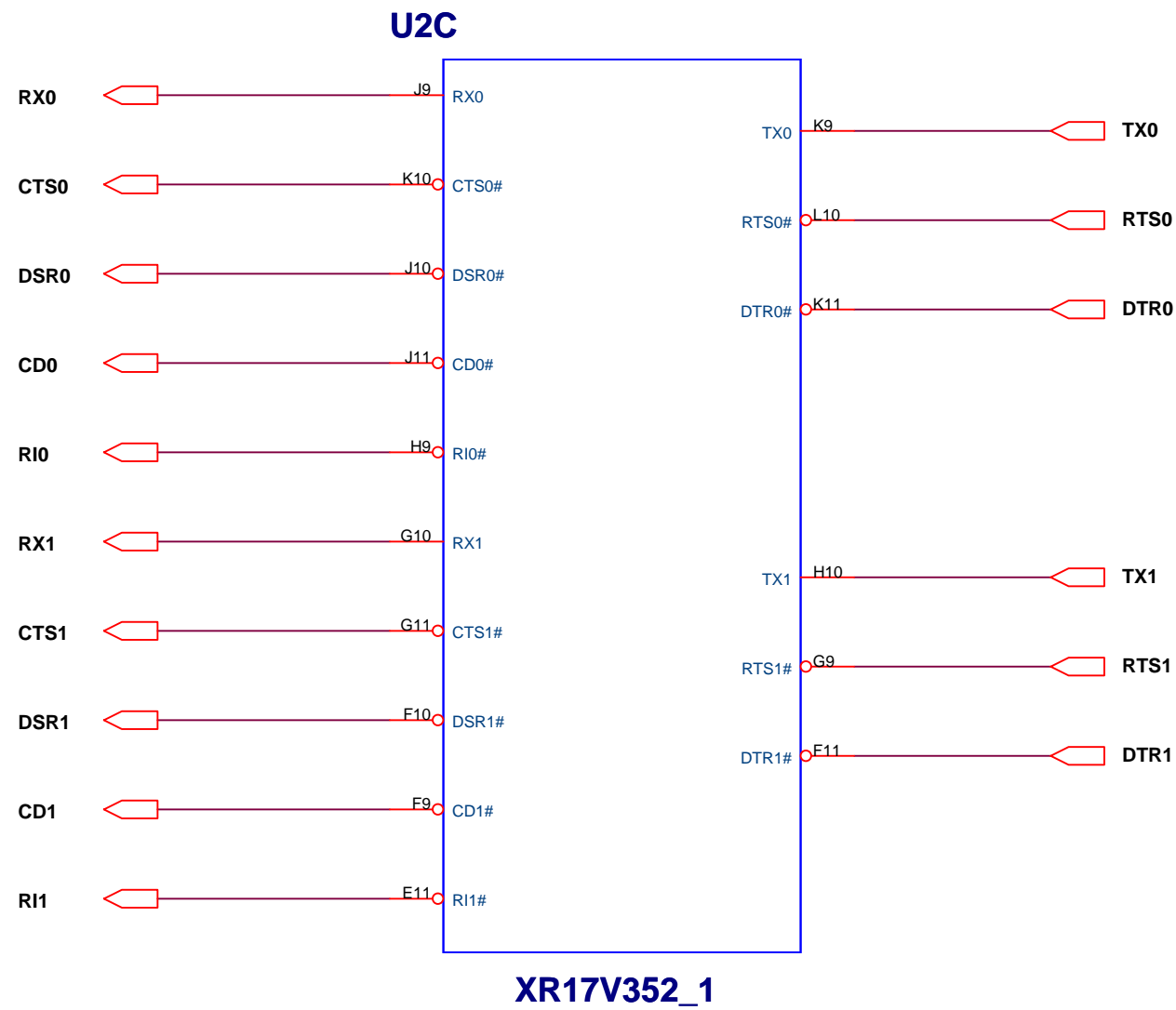
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Size	Document Number	Rev
	17V352 EVALUATION BOARD	1.0
Date:	Friday, December 11, 2009	Sheet 2 of 9



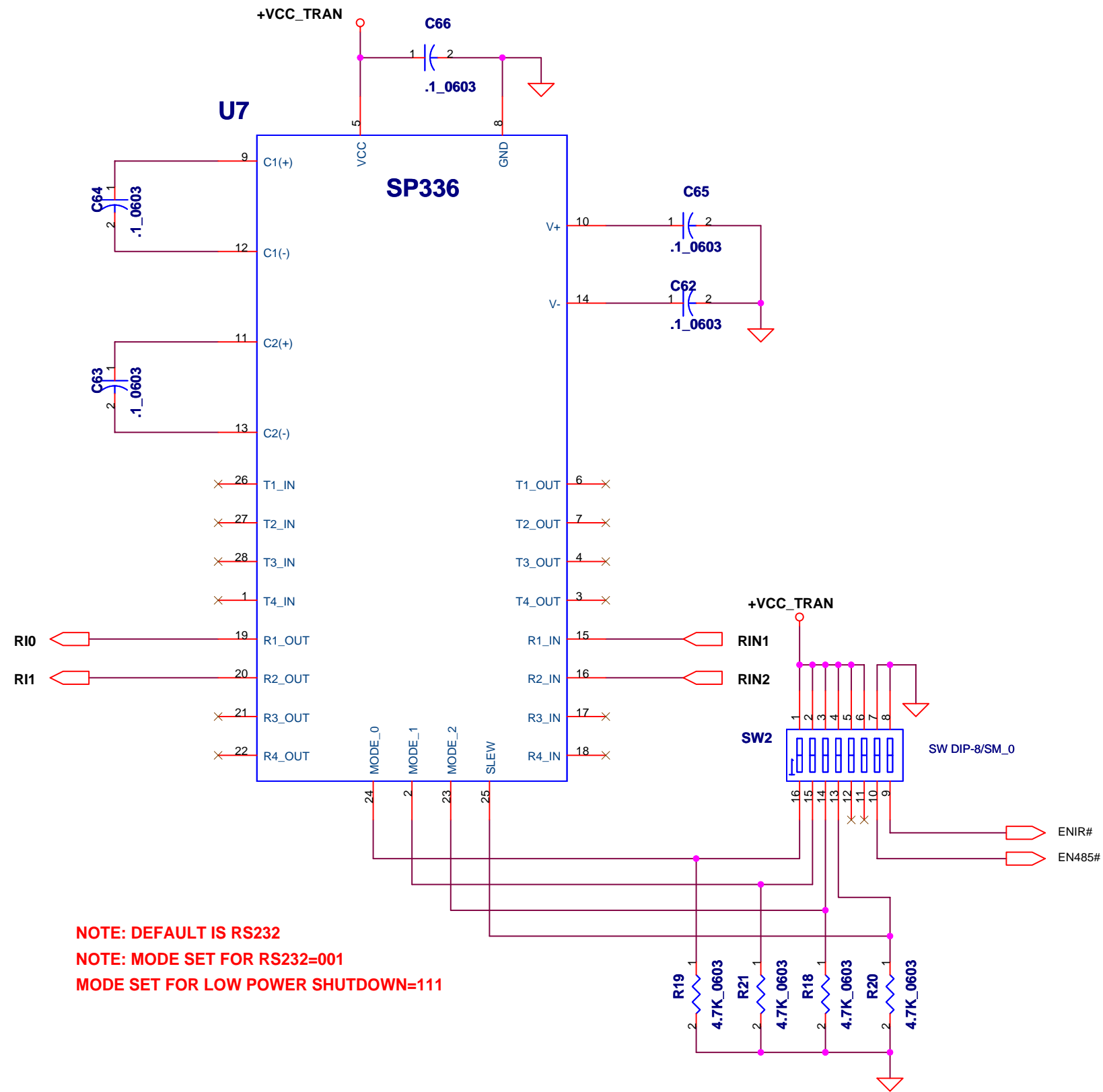
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Size	Document Number	Rev
	17V352 EVALUATION BOARD	1.0
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Title		
17V352 EEPROM JTAG		
Size	Document Number	Rev
Custom	17V352 EVALUATION BOARD	1.0
Date:	Friday, December 11, 2009	Sheet 5 of 9



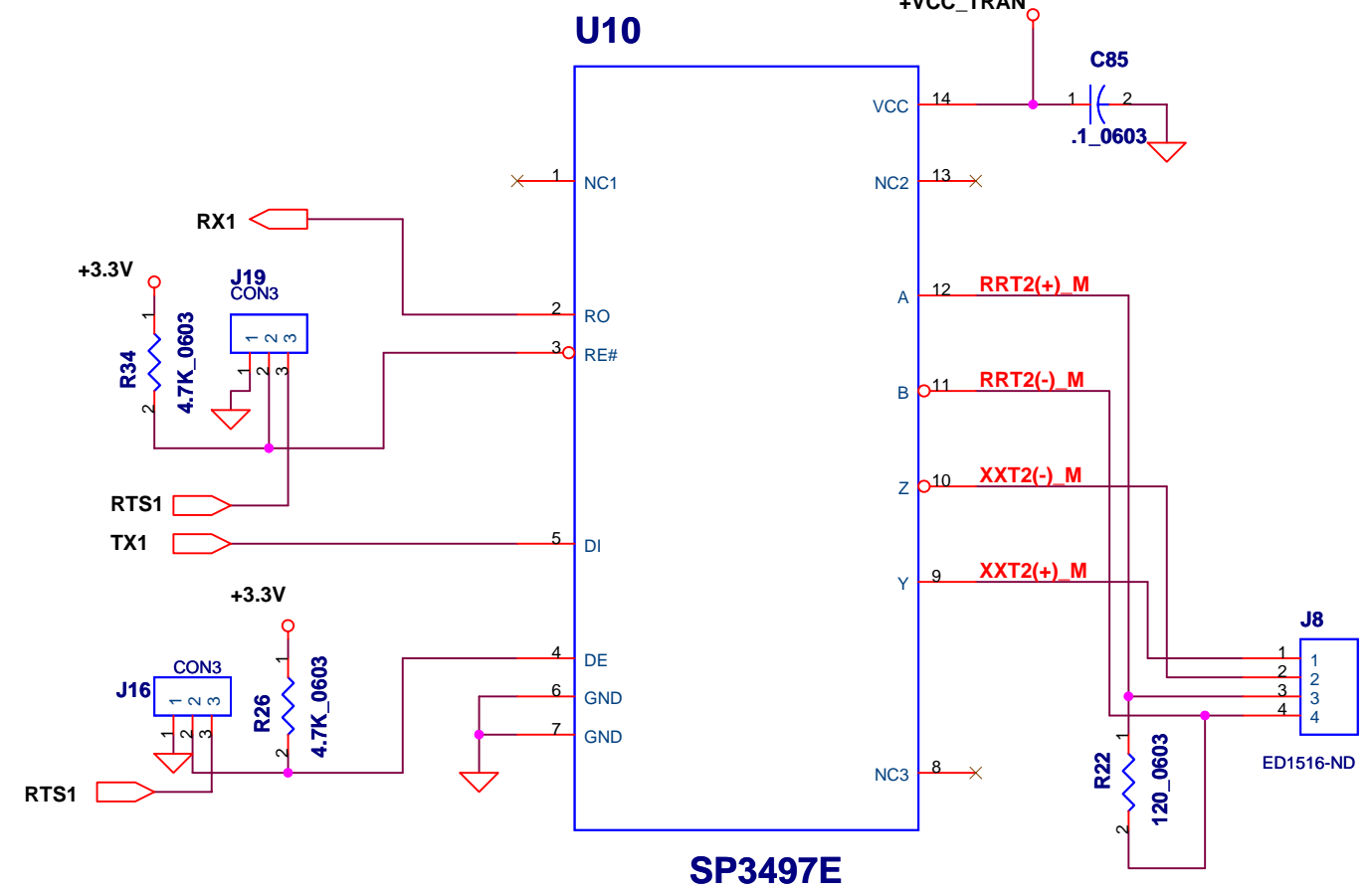
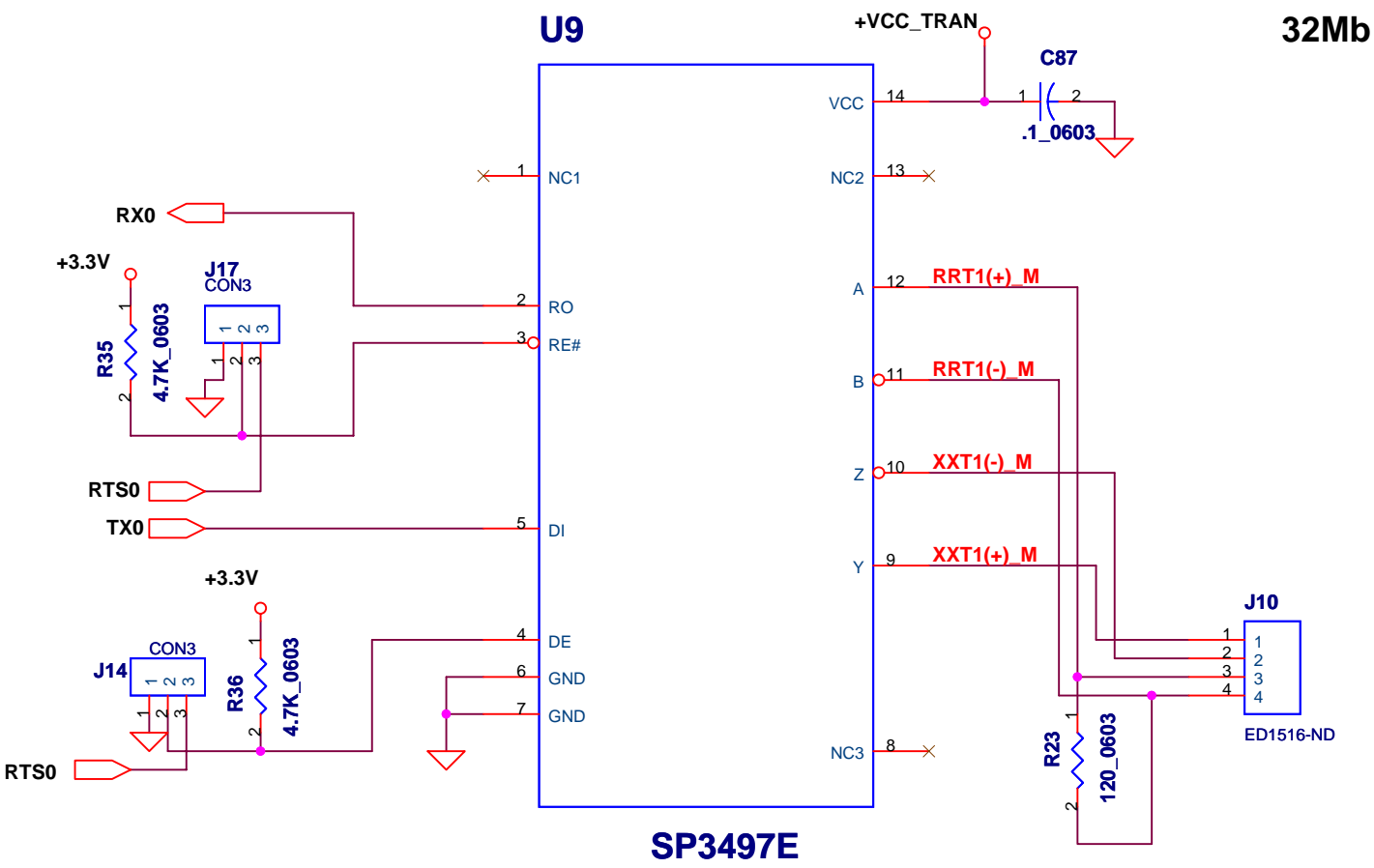
Title		
17V352 MASTER UART SIGNAL		
Size	Document Number	Rev
Custom	17V352 EVALUATION BOARD	1.0
Date:	Friday, December 11, 2009	Sheet 6 of 9



NOTE: DEFAULT IS RS232
 NOTE: MODE SET FOR RS232=001
 MODE SET FOR LOW POWER SHUTDOWN=111

Title		
RS232 (RI)		
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NOTE: NOT INSTALLED



Title		
RS485/422 INTERFACE		
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Custom	XR17V352 EVALUATION BOARD 1.0	
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